



GLSVLSI 2015

Pittsburgh, PA, USA, May 20-22 2015

<http://www.glsvlsi.org/>

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The 25th edition of GLSVLSI will be held in Pittsburgh, Pennsylvania, USA. Original, unpublished papers describing research in the general area of VLSI are solicited. Both theoretical and experimental research results are welcome. Proceedings will be published by the ACM and will be available through the ACM Digital Library. Please visit <http://www.glsvlsi.org/> for more information.

In addition to the traditional topic areas of GLSVLSI listed below, papers are solicited for a **special theme of healthcare and bioengineering, from circuits to systems**. We anticipate a special issue of a journal on the same topic for which selected papers related to the special theme will be invited to submit extended versions for consideration.

Program Tracks:

- **VLSI Design:** design of ASICs, microprocessors/micro-architectures, embedded processors, analog/digital/mixed-signal systems, NoC, interconnects, memories, and FPGAs.
- **VLSI Circuits:** analog/digital/mixed-signal circuits, RF and communication circuits, chaos/neural/fuzzy-logic circuits, high-speed/low-power circuits.
- **Computer-Aided Design (CAD):** hardware/software co-design, logic and behavioral synthesis, logic mapping, simulation and formal verification, layout (partitioning, placement, routing, floorplanning, compaction), algorithms and complexity analysis.
- **Low Power and Power Aware Design:** circuits, micro-architectural techniques, thermal estimation and optimization, power estimation methodologies, and CAD tools.
- **Testing, Reliability, Fault-Tolerance:** digital/analog/mixed-signal testing, design for testability and reliability, online testing techniques, static and dynamic defect- and fault-recoverability, and variation-aware design.
- **Emerging Technologies & Post-CMOS VLSI:** nanotechnology, molecular electronics, quantum devices, biologically-inspired computing, spintronic technology, CNT, SET, RTD, QCA, VLSI aspects of sensor and sensor networks, etc. Emphasis should be on the analysis, novel circuits and architectures, modeling, CAD tools, and design methodologies for emerging technologies.

Paper submission deadline:

December 9, 2014

Acceptance Notification:

February 3, 2015

Camera-Ready Paper Due:

February 24, 2015

Paper Submission: Authors are invited to submit full-length (6 pages maximum), original, unpublished papers along with an abstract of at most 200 words. To enable blind review, the author list should be omitted from the main document. Previously published papers or papers currently under review for other conferences/journals should not be submitted and will not be considered. Electronic submission in PDF format to the <http://www.glsvlsi.org> website is required. Author and contact information (name, affiliation, mailing address, telephone, fax, e-mail) must be entered during the submission process.

Paper Format: Submissions should be in camera-ready two-column format, following the ACM proceedings specifications located at:

<http://www.acm.org/sigs/pubs/proceed/template.html>

and the classification system detailed at: <http://www.acm.org/class/1998>

Paper Publication and Presenter Registration: Papers will be accepted for regular or poster presentation at the symposium. Every accepted paper MUST have at least one author registered to the symposium by the time the camera-ready paper is submitted; the author is also expected to attend the symposium and present the paper.

