



GLSVLSI 2007

Stresa - Lago Maggiore, Italy
March 11-13, 2007

<http://www.glsvlsi.org/>



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The 17th edition of GLSVLSI will be held in Stresa - Lago Maggiore, Italy, the first time in Europe. Original, unpublished papers, describing research in the general area of VLSI are solicited. Both theoretical and experimental research results are welcome. Proceedings will be published by the ACM and will be included in the SIGDA compendium CD-ROM.

Program Tracks:

- *VLSI Design*: design of ASICs, microprocessors and micro-architectures, embedded processors, analog/digital/mixed-signal systems, multi-chip modules, FPGAs.
- *VLSI Circuits*: analog/digital/mixed-signal circuits, RF and communication circuits, chaos/neural/fuzzy-logic circuits, high-speed/low-power circuits.
- *Computer-Aided Design (CAD)*: hardware/software co-design, logic and behavioral synthesis, logic mapping, simulation and formal verification, layout (partitioning, placement, routing, floorplanning, compaction), algorithms and complexity analysis.
- *Low Power and Power Aware Design*: circuits, micro-architectural techniques, thermal estimation and optimization, power estimation methodologies, and CAD tools.
- *Testing, Reliability, Fault-Tolerance*: digital/analog/mixed-signal testing, design for testability and reliability, online testing techniques, static and dynamic defect- and fault-recoverability, and variation-aware design.
- *Emerging Technologies*: nanotechnology, molecular electronics, quantum devices, biologically-inspired computing, single electron transistors, resonant tunneling devices, VLSI aspects of sensor and sensor network, and CAD tools for emerging technology devices and circuits.

Paper submission deadline: November 10, 2006

Acceptance notification: December 22, 2006

Camera-ready paper due: January 8, 2007

Paper Submission: Authors are invited to submit full-length (6 pages maximum), original, unpublished papers along with an abstract of at most 200 words. To enable blind review, the author list should be omitted from the main document. Previously published papers or papers currently under review for other conferences/journals should not be submitted and will not be considered. Electronic submission in PDF format to the <http://www.glsvlsi.org> website is required. Author and contact information (name, street/mailling address, telephone, fax, e-mail) must be entered during the submission process.

Paper Format: To allow reduced turn-around time for accepted papers, GLSVLSI 2007 submissions should be in camera-ready two-column format, following the ACM proceedings specifications located at: <http://www.acm.org/sigs/pubs/proceed/template.html> and the classification system detailed at: <http://www.acm.org/class/1998/>

Paper Publication and Presenter Registration: Papers will be accepted for regular or poster presentation at the symposium. Every accepted paper MUST have at least one author registered to the symposium by the time the camera-ready paper is submitted; the author is also expected to attend the symposium and present the paper.

Journal Special Issue: Authors of the best papers published at the symposium will be invited to submit extended versions of their manuscripts for possible publication in a special issue of some international journal.

Best Student Paper Award: A "Best Student Paper Award", sponsored by Intel Corp., will be assigned by the technical program committee. Only papers with a student as first author will be eligible for receiving the award.