



GLSVLSI 2006

Philadelphia, Pennsylvania
April 30, 2006 – May 2, 2006
<http://www.glsvlsi.org/>



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The 16th edition of GLSVLSI will be held on April 30, 2006 – May 2, 2006 in Philadelphia, Pennsylvania. Original, unpublished papers, describing research in the general area of VLSI are solicited. Both theoretical and experimental research results are welcome. Proceedings will be published by the ACM and will be included on the SIGDA compendium CD-ROM.

Program Tracks:

- *VLSI Design*: design of ASICs, microprocessors and micro-architectures, embedded processors, analog/digital/mixed-signal systems, multi-chip modules, FPGAs.
- *VLSI Circuits*: analog/digital/mixed-signal circuits, RF and communication circuits, chaos/neural/fuzzy-logic circuits, high-speed/low-power circuits.
- *Computer-Aided Design (CAD)*: hardware/software co-design, logic and behavioral synthesis, logic mapping, simulation and formal verification, layout (partitioning, placement, routing, floorplanning, compaction), algorithms and complexity analysis.
- *Low Power and Power Aware Design*: circuits, micro-architectural techniques, thermal estimation and optimization, power estimation methodologies, and CAD tools.
- *Testing, Reliability, Fault-Tolerance*: digital/analog/mixed-signal testing, design for testability and reliability, online testing techniques, static and dynamic defect- and fault-recoverability, and variation-aware design.
- *Emerging Technologies*: nanotechnology, molecular electronics, quantum devices, biologically-inspired computing, single electron transistors, resonant tunneling devices, VLSI aspects of sensor and sensor network, and CAD tools for emerging technology devices and circuits.

Paper submission deadline: January 17, 2006, 5:00 pm (PST)

Acceptance notification: February 20, 2006

Camera ready paper due: March 6, 2006

Paper Submission: Authors are invited to submit full-length (6 pages maximum), original, unpublished papers along with an abstract of at most 200 words. To enable blind review, the author list should be omitted from the main document. Previously published papers or papers currently under review for other conferences/journals will not be considered. Electronic submission in Adobe PDF format to the www.glsvlsi.org website is required. Author and contact information (name, street/mailling address, telephone, fax, e-mail) must be entered during the submission process.

Paper Format: To allow reduced turn-around time for accepted papers, GLSVLSI 2006 submissions should be in camera-ready two-column format, following the ACM proceedings specifications located at: <http://www.acm.org/sigs/pubs/proceed/template.html> and the classification system detailed at: <http://www.acm.org/class/1998/>.

Symposium Presentations: Papers will be accepted for long, short, or poster presentation at the symposium. Every accepted paper must have at least one author register for and attend the symposium.

Best Student Paper Award: A "Best Student Paper Award" will be voted on by the technical program committee. Only papers with a student as first author will be eligible. A laptop, generously donated by Intel, will be awarded to the winner at the symposium.