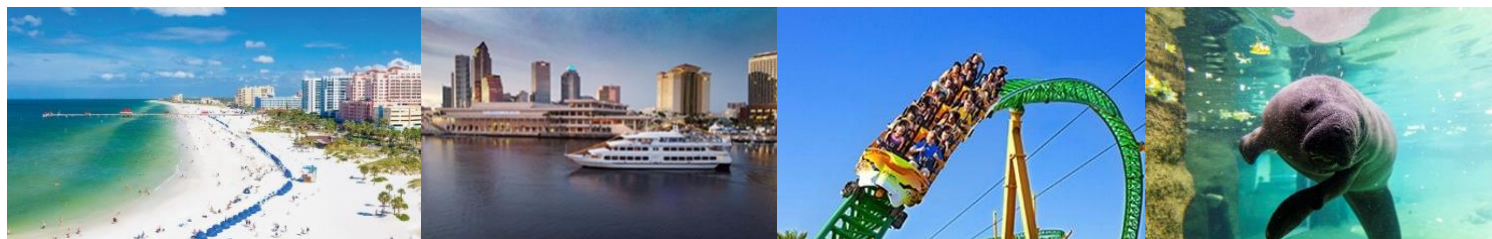


# GLSVLSI 2024

June 12-14, 2024, Tampa Bay Area, FL, USA

<http://www.glsvlsi.org/>

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## Call for Late Breaking Research Papers: 2 page limit

Original, unpublished **late breaking research** papers describing preliminary research in the general areas of VLSI and hardware design are solicited. Please visit <http://www.glsvlsi.org/> for more information.

## Program Tracks:

- **VLSI Circuits and Design:** ASIC and FPGA design, microprocessors/micro-architectures, embedded processors, high-speed/low-power circuits, analog/digital/mixed-signal systems, NoC, SoC, IoT, interconnects, memories, bio-inspired and neuromorphic circuits and systems, BioMEMs, lab-on-a-chip, biosensors, CAD tools for biology and biomedical systems, implantable and wearable devices, machine-learning for VLSI design and optimization
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- **VLSI for Machine Learning and Artificial Intelligence:** hardware accelerators for machine learning, novel architectures for deep learning, brain-inspired computing, big data computing, reinforcement learning, cloud computing for Internet-of-Things (IoT) devices.
- **Microelectronic Systems Education:** Pedagogical innovations using a wide range of technologies such as ASIC, FPGA, multicore, GPU, TPU, educational techniques including novel curricula and laboratories, assessment methods, distance learning, textbooks, and design projects, Industry and academic collaborative programs and teaching.

**Paper submission deadline:**

**March 17, 2024 (11:59pm EST)**

**Acceptance Notification:**

**March 28, 2024**

**Camera-Ready:**

**April 12, 2024**

**Paper Submission:** Authors are invited to submit short (2 pages maximum), original, unpublished papers on late breaking research along with an abstract of at most 200 words. To enable blind review, the author list should be omitted from the main document. Previously published papers or papers currently under review for other conferences/journals should NOT be submitted and will not be considered. Electronic submission in PDF format to the <http://www.glsvlsi.org> website is required. Author and contact information must be entered during the submission process.

**Paper Format:** Submissions should be in camera-ready two-column format, following the ACM proceedings specifications and the classification system (<http://www.acm.org/publications/class-2012>)

**Paper Publication and Presenter Registration:** Papers will be accepted for poster presentations at the symposium. Every accepted paper MUST have at least one registered author by the time the camera-ready paper is submitted; at least one of the authors is expected to present the paper at the symposium.

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